

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Hidetoshi ISHIDA, et al.

Serial No.: 10/768,040

Filed: February 02, 2004



: Customer Number: 20277
: Confirmation Number: 6309
: Group Art Unit:
: Examiner: Unknown

For: FIELD-EFFECT TRANSISTOR, AND INTEGRATED CIRCUIT DEVICE AND SWITCHING CIRCUIT USING THE SAME

FORMAL DRAWINGS TRANSMITTAL

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

At the time the above application was filed, informal drawings were presented with the application.

Seven (7) formal drawings are submitted herewith.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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